GIS | GLOBAL INKJET SYSTEMS

A NANODIMENSION DIVISION

Are you passionate about technology? Do you want to design the future of Manufacturing 4.0?

At Global Inkjet Systems (GIS) we are passionate about the technology involved in industrial inkjet printing and we are a part of revolutionising the way the world manufactures!

As part of Nano Dimension, we are building the industrial printing and manufacturing of the future through the design of tailored next-generation software, ink delivery systems and advanced printing components for our customers globally.

Our people are at the heart of what we do, it's our passion, innovation and ability to work collaboratively, communicate openly and resolve complex challenges that makes us the award-winning company we are within our industry.

The Role

We are looking for a passionate FPGA Design Engineer to join our multi-disciplined engineering team based in North Cambridge. Our products depend on complex FPGAs to provide control functions that drive our partners' print heads and perform image processing required for high-performance printing applications.

We are seeking an exceptional engineer with a passion for FPGA design who enjoys working in a challenging and complex environment with some of the best in the industry.

As an FPGA design engineer you will be involved in:

- VHDL development to meet customer and in-house requirements
- FPGA simulation and testing
- Participation in group design activities
- Taking responsibility for work assigned
- Review of the existing code base to propose and develop new solutions

Required Skills:

- Strong degree level qualification in Engineering, Computer Science or other relevant subjects.
- Strong industrial experience or other relevant experience.
- Strong documentation skills for specifying requirements, architecture, implementation, and verification plans.
- Development using VHDL (preferably) or Verilog
- Efficient logic design targeted for FPGAs
- Constructing self-checking regression test vectors with ModelSim or other leading simulation CAD tool
- Synthesising, design constraining and timing closure using Intel Quartus Prime and/or Xilinx Vivado Design Suite
- A knowledge of embedded C/C++ programming
- Use of version control software
- Self-motivated and can work independently, but co-operative and work well in a team environment

Desirable Skills:

- Experience in development within ARM based SoC systems
- Industry knowledge of Image Processing, Print, Industrial Automation or Inkjet.
- Aware of the latest FPGA technologies, including SoC and high-speed digital communications features such as LVDS and other transceivers
- Some ASIC development experience would be beneficial
- Experience within an embedded Linux environment
- Experience using Jenkins / Python to simulate, test and build
- Experience in using Git / TortoiseGit
- Experience with software IDE tools such as Eclipse or Visual Studio
- Able to understand PCB schematics and electronic component specifications
- Good communications skills, verbal and written, to interface with other disciplines

In addition to a competitive salary, the role offers an attractive benefits package:

- Pension contributions
- Private health insurance
- Life insurance
- Company-funded staff kitchen with snacks, chocolate, drinks and fruit
- Company-funded social events
- Company-funded charity committee
- 25 days annual leave
- Cycle to work scheme